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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of  
Taylor R. Efland

Docket Number: TI-31678

Serial No.: 09/975,630

Art Unit: 2826

Filed: 10/12/2001

Examiner: L. Andujar

For: CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION  
FUNCTIONS OF CIRCUITS AND LEADFRAMES INTO THE CHIP ...

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NAME OF INVENTOR(S): <b>EFLAND</b>		RECEIPT DATE & SERIAL NO.: Serial No.: 09/975,630 Filing Date: 10/12/2001	
TITLE OF INVENTION: <b>CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION...</b>			
TI FILE NO.: <b>TI-31678</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

In re the Application of:

Taylor R. Efland

Serial No.: 09/975,630

Filed: 10/12/2001

For: CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION FUNCTIONS  
OF CIRCUITS AND LEADFRAMES INTO THE CHIP SURFACE

Docket: TI-31678

Art Unit: 2826

Examiner: L. Andujar

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Respectfully submitted,

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